Low Speed, low power Transmitter IP　　RCLTX01
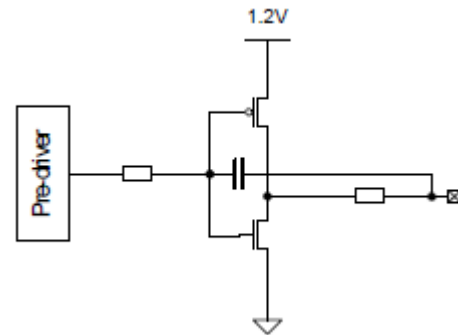
Description

The RCLTX01 is low power, low voltage transmitter IP designed using 0.18um CMOS process. It can be configured and interfaced with other CMOS PHY IPs for performing the controlling functions. It can send the low frequency control signals to various other PHY blocks.

The Low-Power transmitter is a slew-rate controlled push-pull driver. It supports 20Mbps speed.

Functional Diagram

Applications

- Control signal transmitters in PHY
- Clock Drivers
- Data Tx/Rx PHY

Key Features

- Low Power CMOS Design
- Power down mode
- Reconfigurable for use in various high speed PHY designs for control signal transmission.
- Easily portable to other CMOS foundries

DC Specifications:

Parameter	Description	Min	Nom	Max	Units
V_{OH}	Thevenin output high level	1.1	1.2	1.3	V
V_{OL}	Thevenin output low level	-50		50	mV

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AC Specification

Parameter	Description	Min	Nom	Max	Units
T_{RLP}/T_{FLP}	15%-85% rise time and fall time			25	ns
T_{REOT}	30%-85% rise time and fall time			35	ns
$T_{LP-PER-TX}$	Period of the LP exclusive-OR clock	90			ns
C_{LOAD}	Load capacitance	0		70	pF

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